Fig. 1

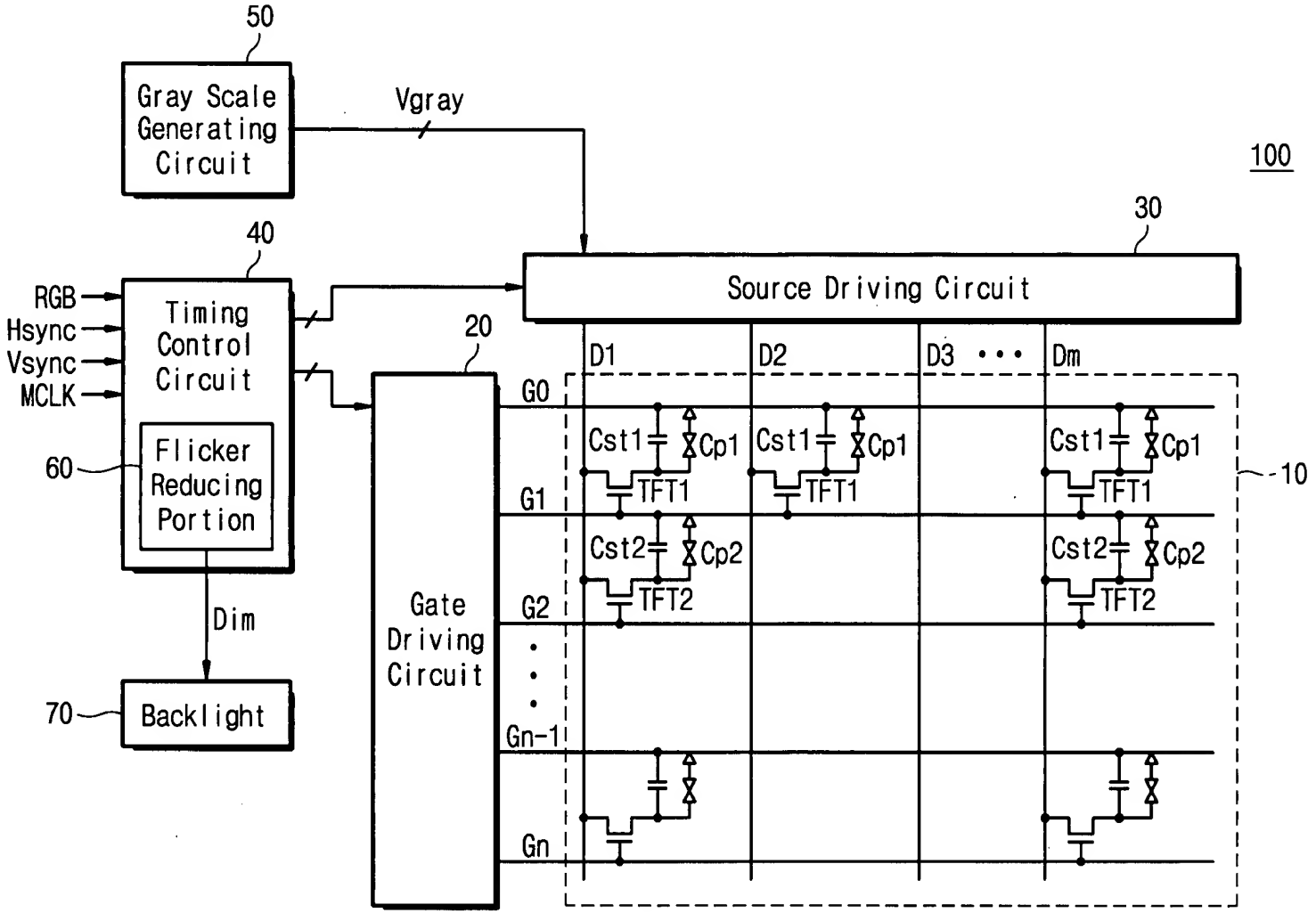

50
Gray Scale Generating Circuit
Vgray
100
30
40
Source Driving Circuit
RGB
Hsync
Vsync
MCLK
Timing Control Circuit
20
D1
D2
D3 • • •
Dm
G0
G1
G2
Gn-1
Gn
Cst1
Cp1
TFT1
Cst2
Cp2
TFT2
10
60
Flicker Reducing Portion
Dim
Gate Driving Circuit
70
Backlight

Fig. 2

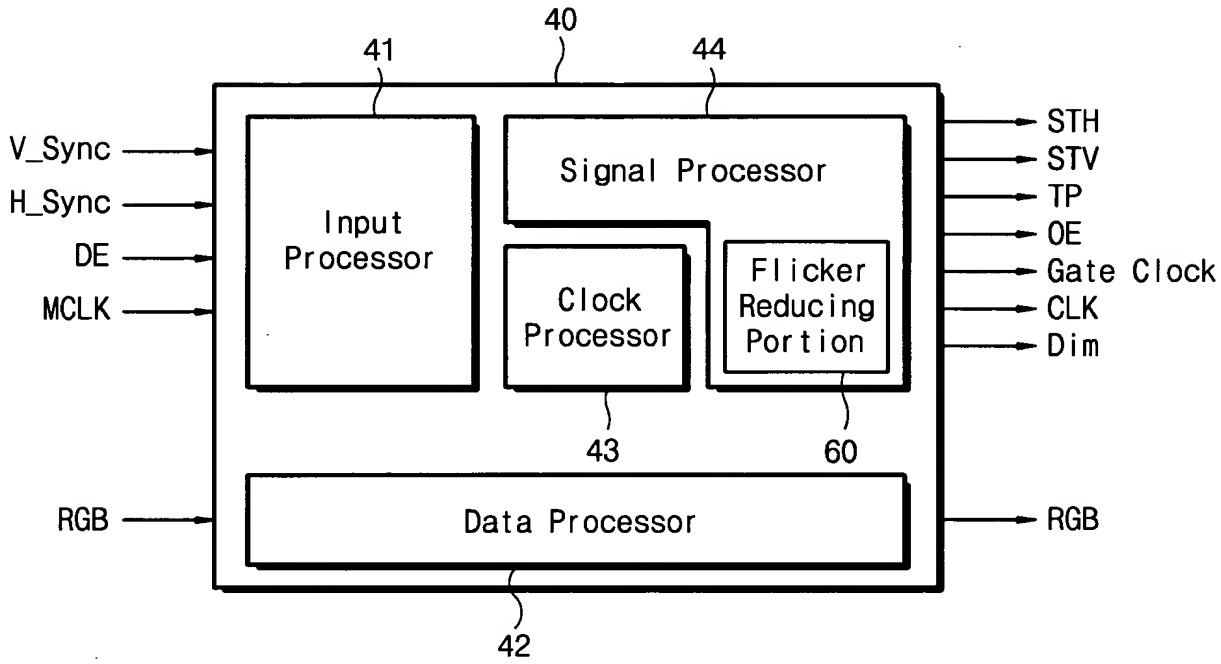

40
41
44
V_Sync
H_Sync
DE
MCLK
Input Processor
Signal Processor
Clock Processor
Flicker Reducing Portion
43
60
RGB
Data Processor
42
STH
STV
TP
OE
Gate Clock
CLK
Dim
RGB

Fig. 3

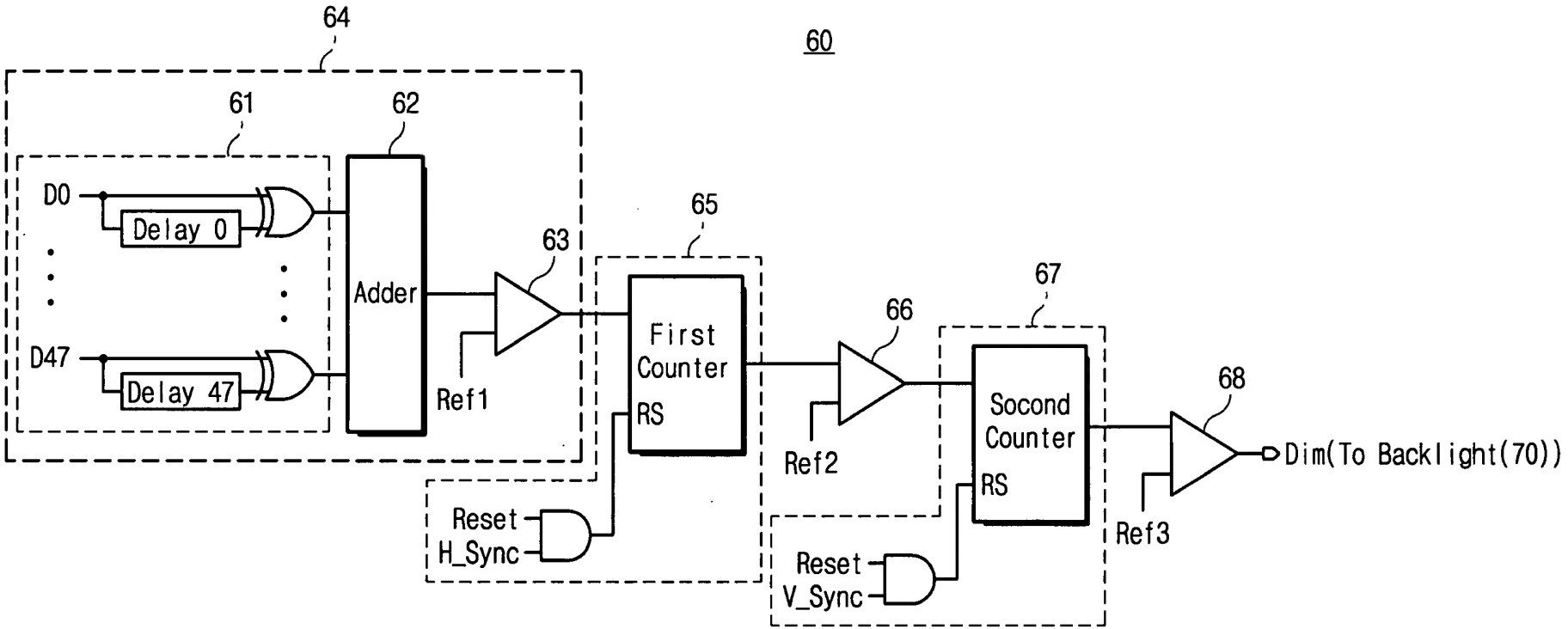
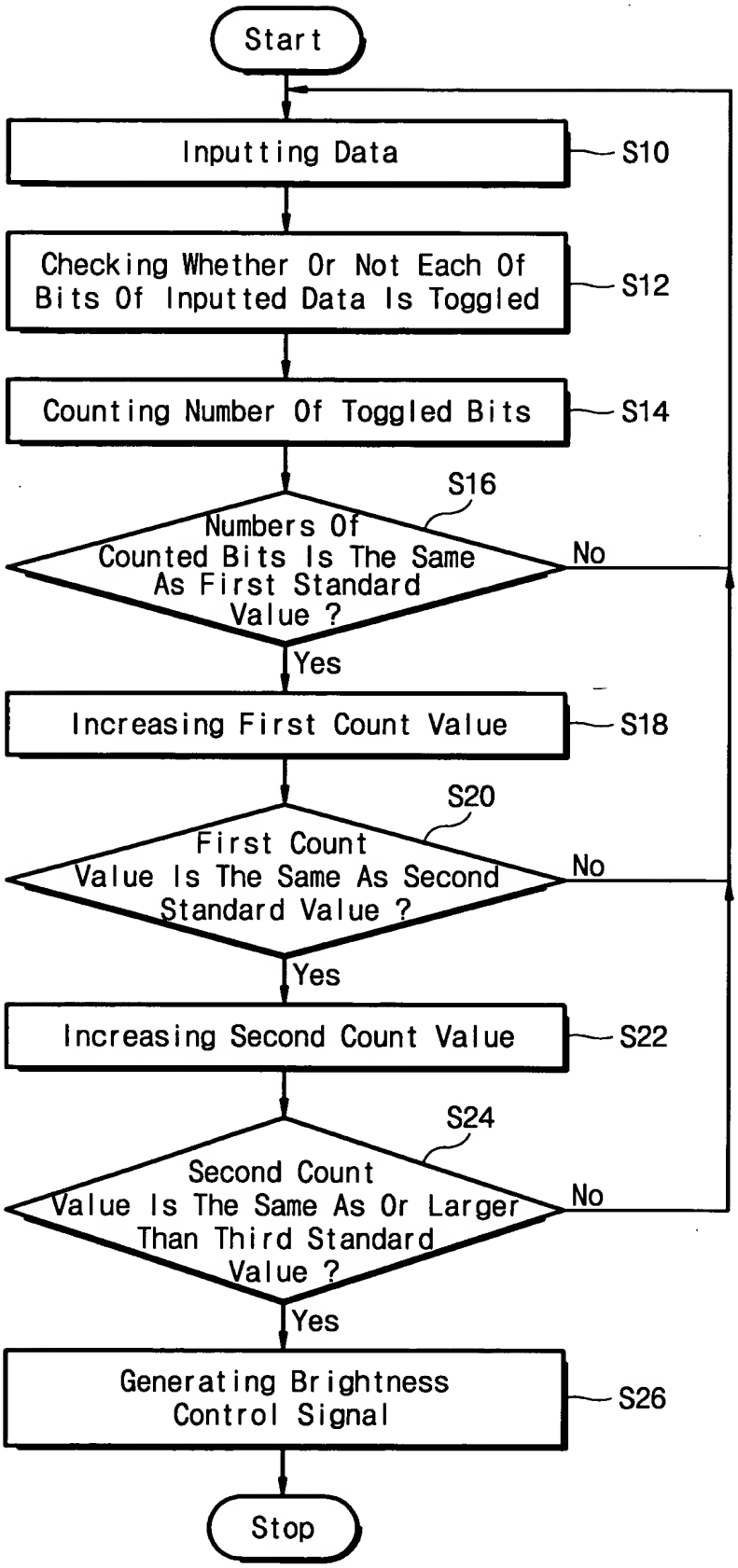

64
60
61
62
D0
Delay 0
D47
Delay 47
Adder
63
Ref1
65
First
Counter
RS
Reset
H_Sync
66
Ref2
67
Socond
Counter
RS
Reset
V_Sync
68
Ref3
Dim(To Backlight(70))

Fig. 4


Start
Inputting Data
S10
Checking Whether Or Not Each Of Bits Of Inputted Data Is Toggled
S12
Counting Number Of Toggled Bits
S14
S16
Numbers Of Counted Bits Is The Same As First Standard Value ?
No
Yes
Increasing First Count Value
S18
S20
First Count Value Is The Same As Second Standard Value ?
No
Yes
Increasing Second Count Value
S22
S24
Second Count Value Is The Same As Or Larger Than Third Standard Value ?
No
Yes
Generating Brightness Control Signal
S26
Stop